

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A liquid crystal device, comprising:
 - a first operation mode conducting sequential driving;
 - a second operation mode conducting simultaneous-multiple driving;
 - a shift register that outputs a shifted ~~signal~~signal, the shift register comprising:
 - a first clocked inverter for inverting an input signal,
 - an inverter for re-inverting the inverted signal,
 - a second clocked inverter for feeding back the re-inverted signal to an
input of the first clock inverter, and
 - an output terminal comprising a NAND gate and an inverter in series;
 - a transfer signal line that outputs a transfer signal based on output from the shift register;
 - an enable signal supply unit that supplies a plurality of enable signals over separate enable lines during the time duration of the pulsewidth of the transfer signal, the enable signals each having a pulsewidth shorter than the pulsewidth of the transfer signal;
 - an enable circuit comprising a NAND gate and an inverter in series that ANDs the transfer signal and the enable signals during the same time duration as the transfer signal to output a plurality of sampling signals during the time duration of the transfer signal, the sampling signals having the same pulsewidth and timing as the pulsewidth and timing of the enable signals;
 - an input unit selecting one of the first operation mode and the second operation mode; and

a control unit switching between the operation modes according to output of the input unit, and that controls the enable signal supply unit to output the enable signal sequentially during the first operation mode and simultaneously during the second operation mode.

2. (Original) The liquid crystal device according to claim 1, the first operation mode that image signals are supplied to at least one of image signal lines without being serial-parallel converted.

3. (Original) The liquid crystal device according to claim 1, the second operation mode that image signals are serial-parallel converted into a plurality of components.

4. (Currently Amended) A liquid crystal device, comprising:
a first operation mode conducting sequential driving;
a second operation mode conducting simultaneous-multiple driving;
a shift register that outputs a shifted ~~signal; signal~~, the shift register comprising:
a first clocked inverter for inverting an input signal,
an inverter for re-inverting the inverted signal,
a second clocked inverter for feeding back the re-inverted signal to an
input of the first clock inverter, and
an output terminal comprising a NAND gate and an inverter in series;
a transfer signal line that outputs a transfer signal based on output from the shift register;
an enable signal supply unit that supplies a plurality of enable signals over separate enable lines during the time duration of the pulsewidth of the transfer signal, the enable signals each having a pulsewidth shorter than the pulsewidth of the transfer signal;
an enable circuit comprising a NAND gate and an inverter in series that ANDs the transfer signal and the enable signals during the same time duration as the transfer signal

to output a plurality of sampling signals during the time duration of the transfer signal, the sampling signals having the same pulsewidth and timing as the pulsewidth and timing of the enable signals;

a motion detector detecting the presence or absence of motion in an image to be displayed; and

an image signal processing circuit switching between the operation modes according to the detection result of the motion detector, and that controls the enable signal supply unit to output the enable signal sequentially during the first operation mode and simultaneously during the second operation mode.

5. (Original) The liquid crystal device according to claim 4, the first operation mode that image signals are supplied to at least one of image signal lines without being serial-parallel converted.

6. (Original) The liquid crystal device according to claim 4, the second operation mode that image signals are serial-parallel converted into a plurality of components.

7. (Original) The liquid crystal device according to claim 4, further comprising an input unit setting whether an image to be input as a video signal or an image to be input as an RGB signal is displayed.

8. (Original) The liquid crystal device according to claim 4, the image signal processing circuit switching to the first operation mode when there is any motion contained in an image represented by the input image signal.

9. (Original) The liquid crystal device according to claim 4, the image signal processing circuit switching to the first operation mode when there is rapid motion contained in an image represented by the input image signal.

10. (Original) The liquid crystal device according to claim 4, the image signal processing circuit switching to the second operation mode when there is no motion detected in the image to be displayed.

11. (Original) The liquid crystal device according to claim 4, the image signal processing circuit switching to the second operation mode when there is some motion detected in the image to be displayed.

12.-15. (Canceled)